DERWENT-ACC-NO: 1999-596749

**DERWENT-WEEK: 200032** 

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TITLE: <u>Bevel</u> surface processing used in <u>semiconductor</u> wafer manufacture involves <u>polishing bevel</u> surface of <u>semiconductor</u> wafer, to set its surface roughness to specific value

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**PATENT-FAMILY:** 

PUB-NO PUB-DATE LANGUAGE PAGES MAIN-IPC
JP 11260775 A September 24, 1999 N/A 004 H01L 021/304

**APPLICATION-DATA:** 

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INT-CL (IPC): H01L021/304

ABSTRACTED-PUB-NO: JP 11260775A

BASIC-ABSTRACT: NOVELTY - Polysilicon film (2) is formed surrounding the <u>semiconductor</u> wafer (1). Polysilicon film in the <u>bevel</u> surface (11) of the <u>semiconductor</u> wafer is removed and <u>polished</u> so that its surface roughness is set to be 400 Angstrom or less.

USE - In <u>semiconductor</u> wafer manufacture.

ADVANTAGE - The bad influence of <u>bevel</u> surface on epitaxial growth or device production process is prevented, by setting its roughness to specific value. Thus, productivity is improved.

DESCRIPTION OF DRAWING - The figure shows partially enlarged sectional view of <u>semiconductor</u> wafer in each process of manufacturing method. (1) <u>Semiconductor</u> wafer; (2) Polysilicon film; (11) <u>Bevel</u> surface.

CHOSEN-DRAWING: Dwg.1/3

TITLE-TERMS:

BEVEL SURFACE PROCESS SEMICONDUCTOR WAFER MANUFACTURE POLISH
BEVEL SURFACE

SEMICONDUCTOR WAFER SET SURFACE ROUGH SPECIFIC VALUE

**DERWENT-CLASS: L03 U11** 

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